

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	174	reconfigur\$4 with system near3 chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 15:08
L2	4	(reconfigur\$4 with system near3 chip) same resource	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 15:02
L3	0	(reconfigur\$4 with system near3 chip) same allocat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 15:02
L4	14	1 and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 15:03
L5	2	reallocation near3 hardware near3 resources	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 15:09
L6	26	reallocation near3 available near3 resources	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 15:20
L7	631	(hwang-l\$ sanchez-r\$).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 15:21
L8	29	7 and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 15:21
S65	711	((716/16) or (716/17)).CCLS.	USPAT; USOCR	OR	OFF	2005/06/23 10:19